

Docket No.: W&B-INF-1892

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : ANDREAS JAKOBS ET AL.  
Filed : CONCURRENTLY HEREWITH  
Title : PSEUDOSTATIC MEMORY CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 6,392,958 B1 (Lee), dated May 21, 2002;

U.S. Patent Application Publication No. 2002/0027821 A1 (Mizugaki), dated March 7, 2002;

Sawada, K. et al.: "A 30-  $\mu$ A Data-Retention Pseudostatic RAM with Virtually Static RAM Mode", IEEE Journal of Solid-State Circuits, Vol. 23, No. 1, February 1988, pp. 12-19.

Respectfully submitted,



For Applicants  
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<b>FORM PTO-1449 (SUBSTITUTE)</b>  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT</b> (37 CFR 1.98(b))				Attorney Docket No.: W&B-INF-1892 Appl. No.: _____  Applicant: ANDREAS JAKOBS ET AL.  Filing Date: September 30, 2003 Group Art Unit: _____			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	6,392,958 B1	5/21/02	Lee			
	B	2002/0027821 A1	3/7/02	Mizugaki			
	C						
	D						
	E						
	F						
	G						
	H						
	I						
<b>FOREIGN PATENT DOCUMENT</b>							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J						
	K						
	L						
	M						
	N						
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
		Sawada, K. et al.: "A 30- $\mu$ A Data-Retention Pseudostatic RAM with Virtually Static RAM Mode", IEEE Journal of Solid-State Circuits, Vol. 23, No. 1, February 1988, pp. 12-19					
<b>EXAMINER</b>				<b>DATE CONSIDERED</b>			